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TI 37201

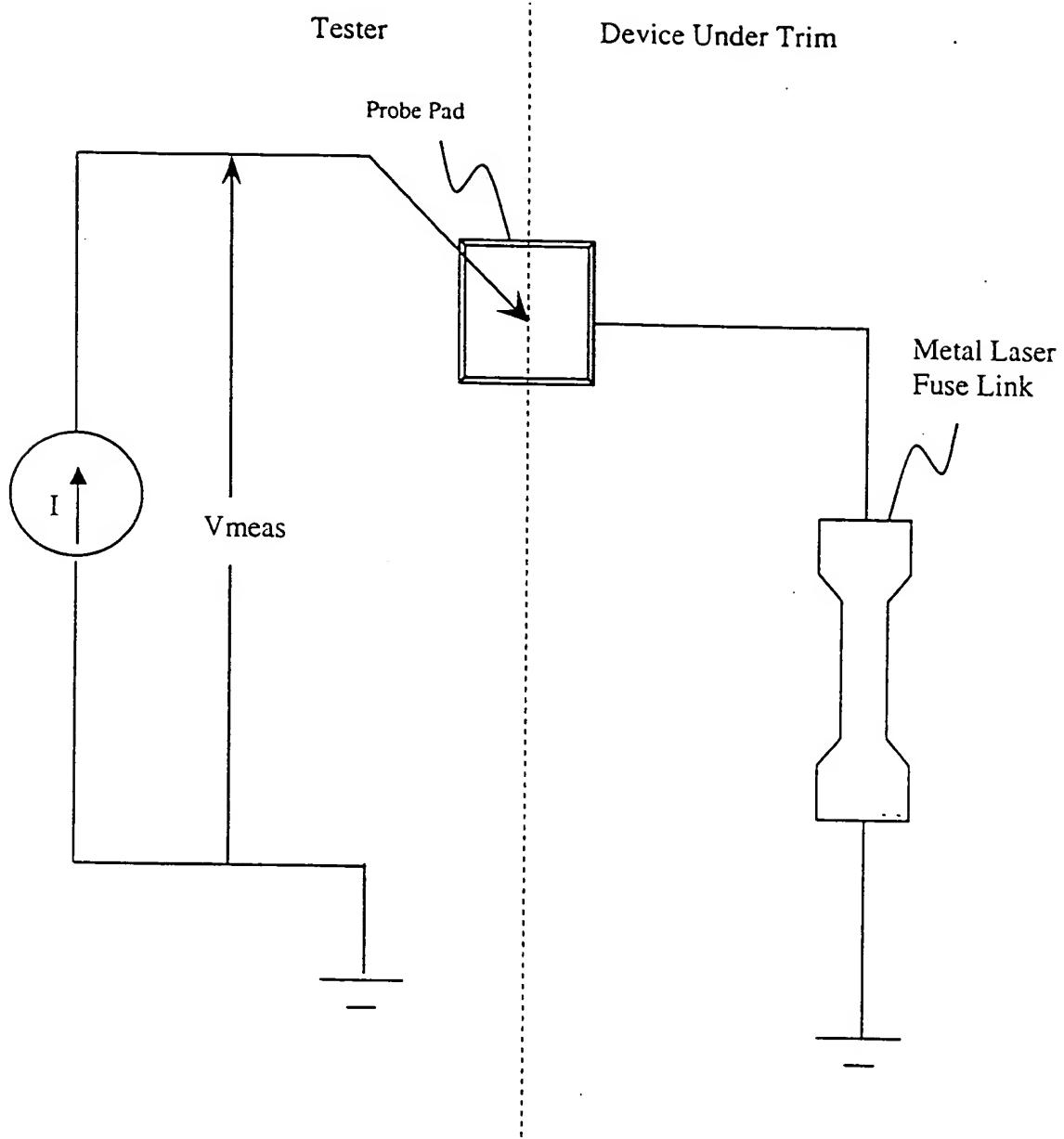


Figure 1  
(Prior Art)

TI 37201

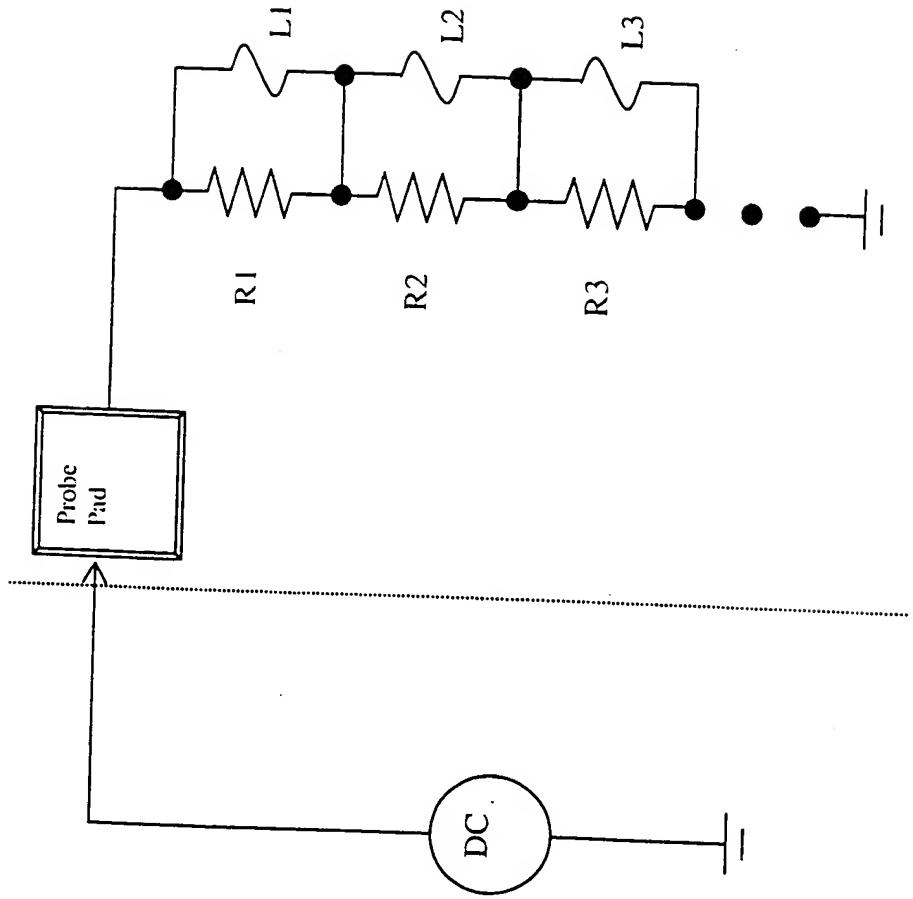


Figure 2

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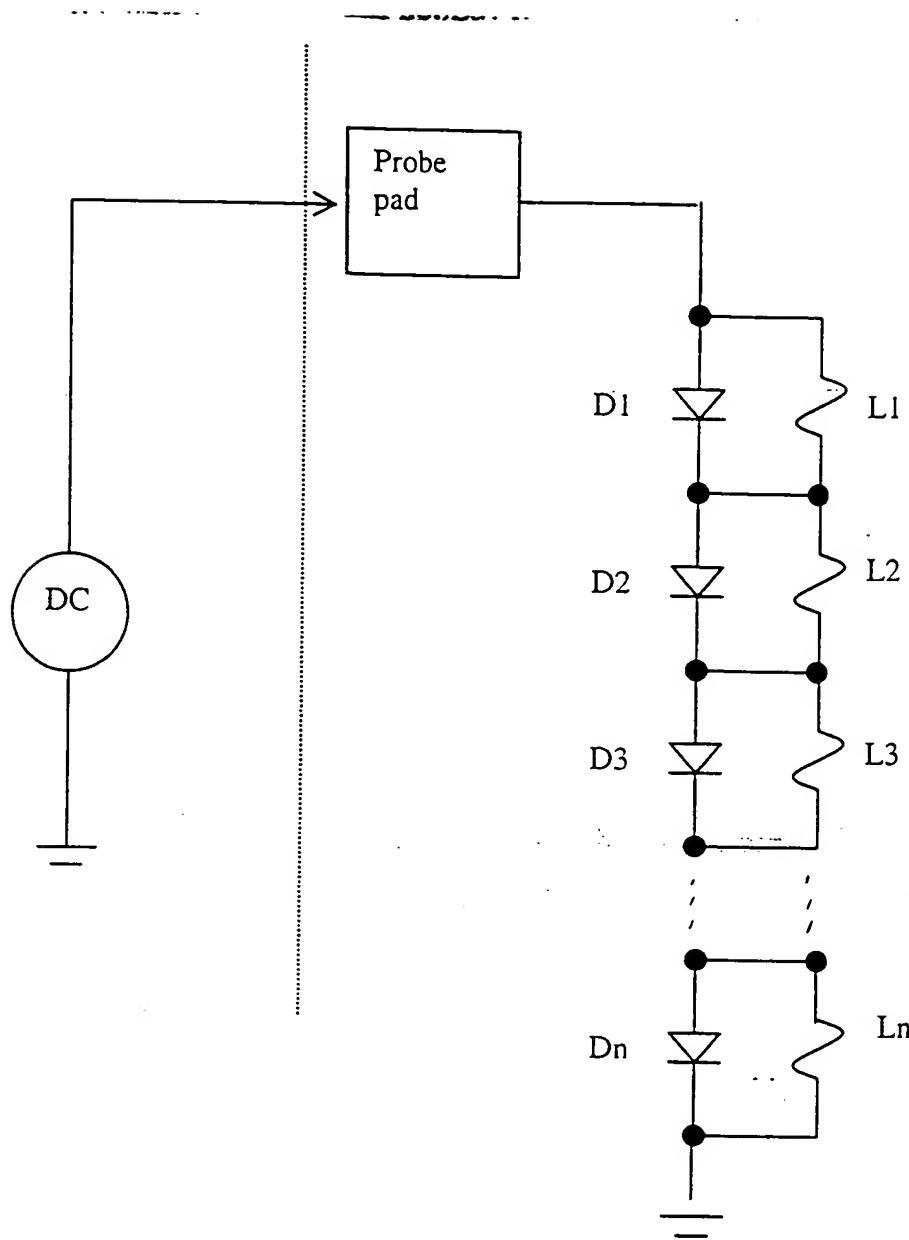


Figure 3

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### Maximum Number of Component/Link Trim Record Pairs per Network

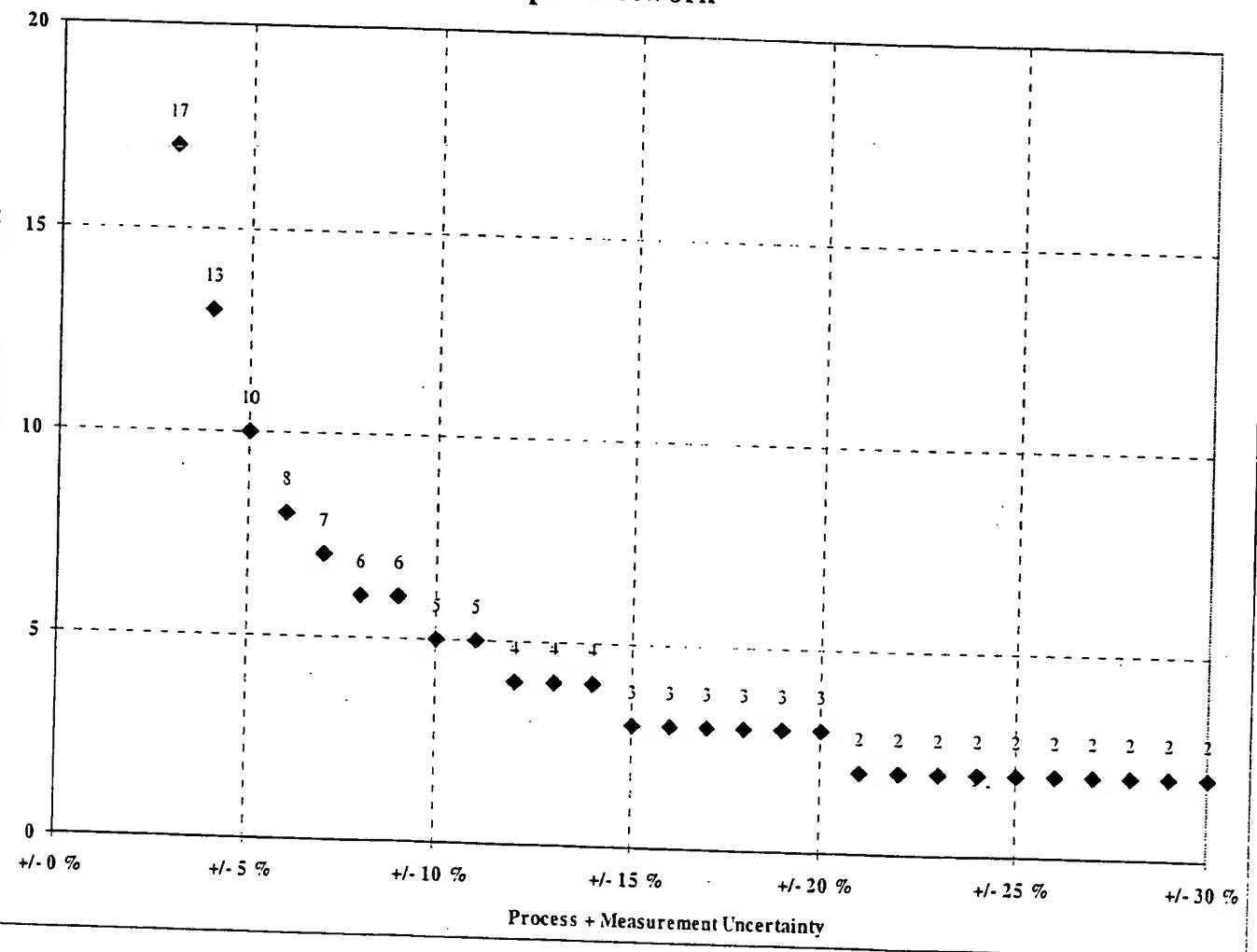


Figure 4

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**Test Equipment**



**Circuit structure**

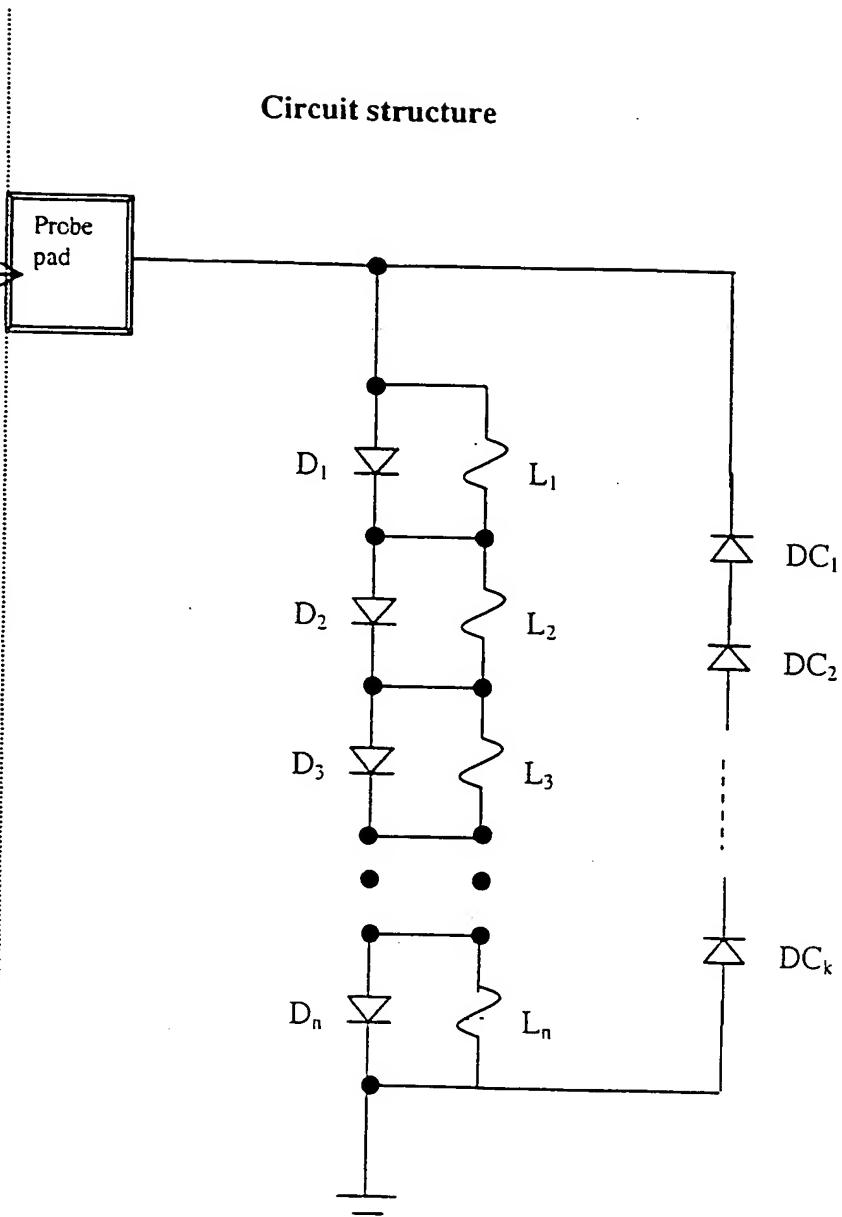


Figure 5

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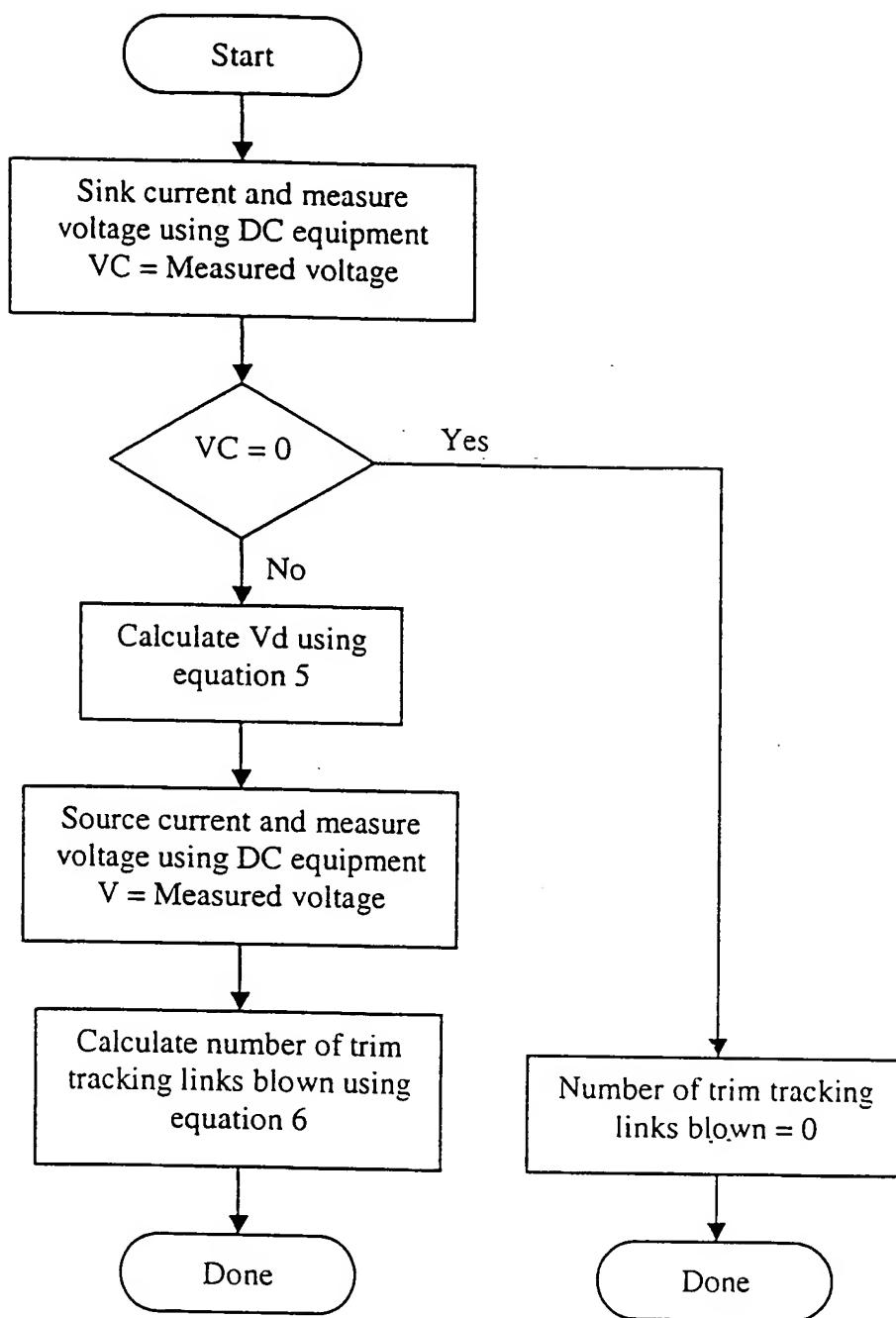


Figure 6

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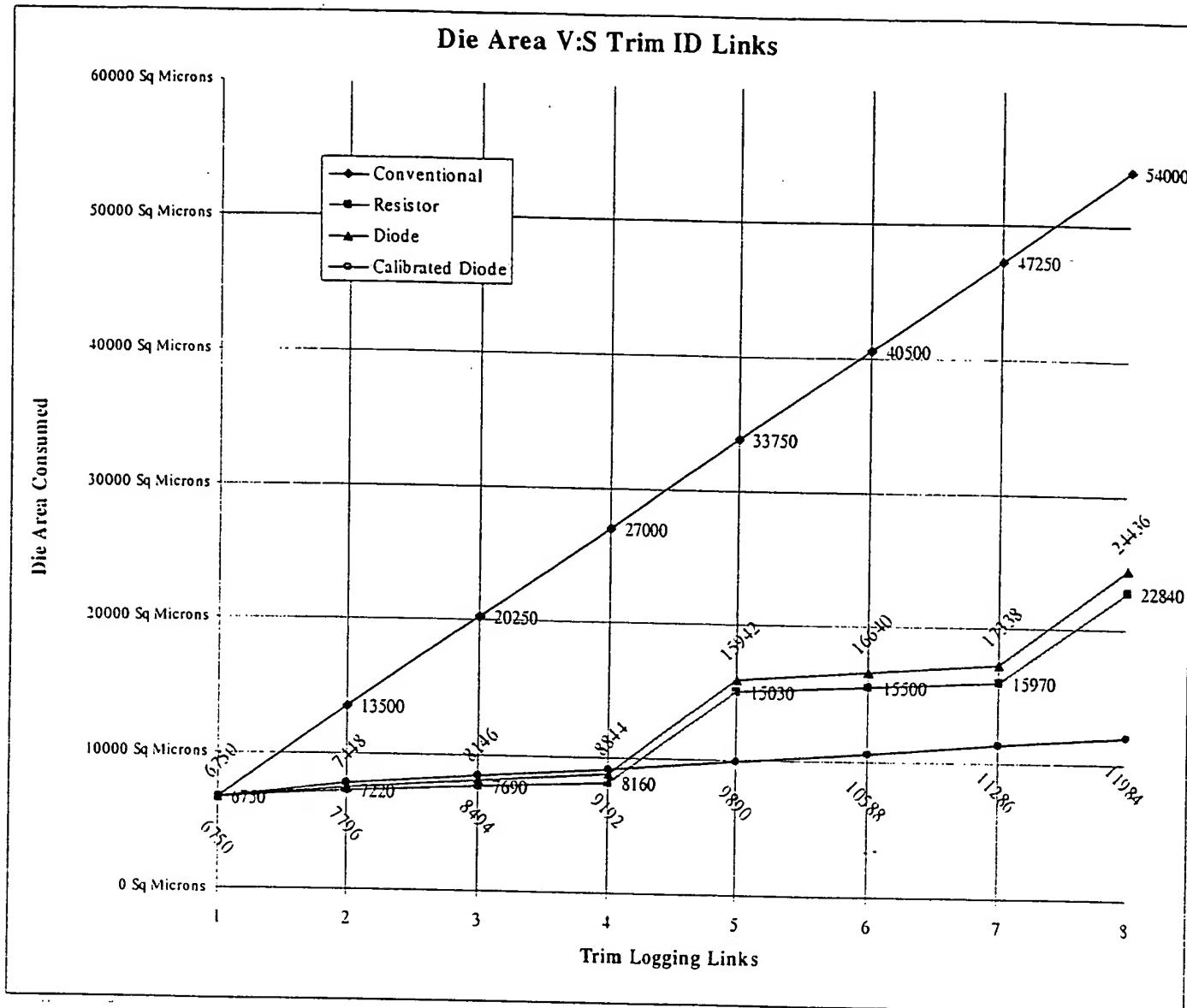


Figure 7